Compact Optical Receivers for Coherent Optical Communication

Masaru TAKECHI*, Yoshihiro TATEIWA, Munetaka KUROKAWA, Hideki YAGI, and Hiroshi HARA

Digital coherent optical communication technology using multi-level modulation formats has been adopted in long haul systems as a crucial solution to the rapidly increasing optical traffic. This technology is expected to further expand to metro systems, where smaller optical transceivers are required. We have developed compact optical receivers that can be installed in the CFP2-ACO optical transceivers, complying with the OIF implementation agreement for Micro-ICR. The new optical receivers have achieved a high responsivity in a small package of $12.0 \times 22.7 \times 4.5$ mm due to an InP-based PIC that consists of waveguide PDs and a 90 degree hybrid. They also include a VOA and signal monitor PD. The digital coherent transmission was verified by the successful demodulation of 128 Gbit/s DP-QPSK modulation and 224 Gbit/s DP-16QAM. This paper presents the design and typical characteristics of the compact optical receivers.

Keywords: Optical receiver, coherent optical communication, InP, ICR, CFP2-ACO

1. Introduction

Communication traffic is increasing continuously along with the expansion of video streaming service and the popularization of smartphones and other mobile devices requiring high-speed, large-capacity transmission. Digital coherent optical communication technology using multilevel modulation formats has already been used for long haul systems and is now attracting attention as a means of long-distance, large-capacity optical communication that meets the demand for the increase in communication traffic. To expand the application of this technology to metro systems in the future, it is critical to downsize the optical transceivers and thereby increase port density and transmission capacity per port. A CFP2-ACO*1 optical transceiver⁽¹⁾ complying with the Optical Internetworking Forum (OIF)*2 implementation agreement is drawing particular attention as a promising device that can meet the above requirement.

Since 2013, Sumitomo Electric Industries, Ltd. has continued to develop compact optical receivers^{(2),(3)} comprising InP-based photonic devices for coherent optical communication applications. We have recently developed a further downsized optical receiver that can be installed in the CFP2-ACO optical transceiver. The new optical receiver conforms to the OIF implementation agreement for micro-intradyne coherent receivers (Micro-ICR).^{*3, (4)}

This paper discusses the design and principal characteristics of the new optical receiver.

2. Configuration of New Optical Receiver

2-1 InP-based photonic integrated circuit

The top view and functional block diagram of the InP-based photonic integrated circuit (PIC) are shown in Figs. 1 (a) and (b), respectively.

The InP-based PIC is a 1.6×4.1 mm InP chip on which four waveguide photodiodes (PDs) and a 90° hybrid



Fig. 1. InP-based PIC

are integrated. The 90° hybrid has the advantage of eliminating the need for crossing output optical waveguides by combining a 2×4 MMI^{*4} and 2×2 MMI.⁽⁵⁾ The new optical receiver achieves a high optical responsivity by butt jointing (BJ) an optical waveguide comprising a GalnAsP core layer and a PD comprising a GalnAs light-absorbing layer.⁽⁶⁾ The spot-size converter (SSC) located in the optical input port efficiently couples the incident light with the optical waveguide layer.⁽⁷⁾ Metal insulator metal (MIM) capacitors are integrated at the PD bias terminal to bypass high-frequency signals and thereby ensure an exceptional high-frequency characteristic.⁽⁸⁾

2-2 Configuration of optical receiver module

The external appearance of the optical receiver module is shown in Fig. 2 (a), and its configuration diagram is shown in Fig. 2 (b).⁽⁹⁾



Fig. 2. Optical Receiver Module

The signal light and local light emitted from an optical fiber are converted to collimate light by a lens. The signal light passes through the beam splitter (BS) for the power monitor PD. After passing through the variable optical attenuator (VOA), the signal light is separated into two orthogonally polarized waves by the polarizing beam splitter (PBS). Subsequently, the straight-advancing light passes through a skew correction device (SC) that corrects optical path difference between the two polarized waves, and is concentrated by lenses into the input waveguide of an InP-based PIC. The other light passes through a halfwave plate to align the polarization directions, and is then deflected by a mirror and concentrated by lenses into the input waveguide of another InP-based PIC. On the other hand, the local light passes through an optical polarizer to align the polarization directions, and is led into two optical paths by the BS. Then the local light is concentrated into the input waveguides of two InP-based PICs in the same manner as the signal light, excepting that the local light does not pass through a half-wave plate.

Each light is converted into an electric current (signal) by the PD in each InP-based PIC, and then amplified by a trans-impedance amplifier (TIA). After passing through a capacitor that removes DC components, the electric signal is output from the high frequency terminal of the package and transmitted to the post-stage electric circuit.

The dimension of the package is $12.0 \times 22.7 \times 4.5$ mm. Its external shape and pin arrangement conform to the OIF implementation agreement for Micro-ICR Type 1 to allow for installation in a CFP2-ACO optical transceiver.

2-3 Development target specifications

The target specifications set for developing the new compact optical receiver are shown in Table 1.

The above specifications ensure the necessary and sufficient characteristics that are required to receive 100 Gbit/s dual-polarization quadrature phase-shift-keying (DP-QPSK)*⁵ modulated signals and 200 Gbit/s quadrature amplitude modulation (DP-16QAM)*⁶ modulated signals that are widely used for digital coherent optical communi-

Table 1. Development Target Specifications

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Item	Condition	Min	Max	Unit
Operating temperature		-5	85	°C
Operating frequency	· C-band	191.35	196.20	THz
Operating wavelength		1528.0	1566.7	nm
Responsivity	Signal light	50	-	mA/W
	LO light	50	-	mA/W
Bandwidth		20	_	GHz
Common mode rejection ratio (CMRR)	DC, Signal light	-	-20	dB
	~22 GHz, Signal light	-	-16	dB
	DC, LO light	-	-12	dB
	~22 GHz, LO light	-	-10	dB
VOA attenuation		10	-	dB

cation. For the responsivity, in particular, a relatively high target value has been set on the supposition that this optical receiver may be used for signal transmission over a distance of 80 km without using any optical amplifier. The new optical receiver ensures high responsivity by using InP-based PICs, each consisting of waveguide PDs and a 90° hybrid.

3. Characteristics of Compact Optical Receiver

3-1 Responsivity

The wavelength dependences at 25°C of the LO light and signal light responsivity of the new compact optical receiver are shown in Figs. 3 (a) and (b), respectively. The figures show that this optical receiver ensures high signal light and local light responsivities of more than 70 mA/W in the operating wavelength range.



Fig. 3. Wavelength Dependence of Responsivity

Figure 4 shows the wavelength dependence of the DC common mode rejection ratio (CMRR). This figure verifies that the new optical receiver demonstrates a superior characteristic of less than -25 dB within its operating wavelength range. For LO light, the characteristics are similar to those obtained for the signal light.

The temperature dependence of responsivity variation is shown in Fig. 5. This graph plots the variation of mean responsivity at a wavelength of 1,550 nm at temperatures of -5°C and 85°C, with responsivity of 25°C taken as a reference.

It was confirmed from the graph that the responsivity variation in a package temperature range from -5 to 85° C is less than $\pm 5\%$.



Fig. 4. Wavelength Dependence of DC CMRR



Fig. 5. Temperature Dependence of Responsivity Variation

3-2 VOA characteristic

The VOA voltage dependence of optical attenuation is shown in Fig. 6, and the mean optical attenuation dependence of the difference in optical attenuation between Xand Y-polarization is shown in Fig. 7.

An optical attenuation of more than 20 dB was obtained by using a MEMS*⁷ VOA. The difference in attenuation between X- and Y- polarization was less than 0.6 dB. It has also been confirmed that, in a package temperature range from -5 to 85°C, the temperature dependences of optical attenuation and the difference in optical attenuation between X- and Y-polarization are less than ± 0.5 dB.

3-3 High frequency characteristic

The frequency dependence of optoelectric conversion gain is shown in Fig. 8. The 3 dB bandwidth is more than 27 GHz, which is sufficient for receiving modulated signals



Fig. 6. VOA Voltage Dependence of Optical Attenuation



Fig. 7. Mean Optical Attenuation Dependence of Difference in Optical Attenuation between X- and Y-polarization



Fig. 8. Frequency Characteristic of Optoelectric Conversion Gain

whose symbol rate is 32 GBaud. Gain fluctuation between four channels was also even at a low level of less than ± 1 dB.

The frequency dependence of the CMRR is shown in Fig. 9. In a frequency range of up to 22 GHz, the CMRR is less than -27 dB, which verifies that this characteristic gives a sufficient margin for the target development specifications.



Fig. 9. Frequency Characteristic of CMRR

3-4 Transmission characteristic

The demodulation constellations of a 128 Gbit/s DP-QPSK modulated signal and 224 Gbit/s DP-16QAM modulated signal are shown in Figs. 10 (a) and (b), respectively.

In DP-QSPK modulation and DP-16QAM modulation, the optical signal-to-noise ratios (OSNRs) of signal light are 20 dB and 41 dB, respectively. Symbols are clearly identified in both modulation systems, verifying that the new optical receiver ensures significantly high signal transmission characteristics.



(b) 224 Gbit/s DP-16QAM

Fig. 10. Demodulation Constellation

4. Conclusion

We have developed a compact optical receiver that can be installed in a CFP2-ACO optical transceiver. The new optical receiver conforms to the OIF implementation agreement for Micro-ICR. Use of InP-based PICs each comprising waveguide PDs and a 90° hybrid has enhanced the responsivity and reduced the size of the new receiver. The VOA and signal monitor PD installed in the new receiver further sophisticate its function. The practicability of the new receiver for digital coherent transmission was verified by the successful demodulation of 128 Gbit/s DP-QPSK and 224 Gbit/s DP-16QAM modulated signals.

Technical Terms

- *1 CFP2-ACO: A standard for compact optical transceivers, established by OIF, for coherent optical communication application. OIF evolved this standard from CFP2, an industry standard for compact optical transceivers that have been standardized as CFP (100G Form-Factor Pluggable). ACO stands for analog coherent optics.
- *2 OIF: An abbreviation of Optical Internetworking Forum, an industry association for optical networking.
- *3 Micro-ICR: An abbreviation of micro-intradyne coherent receivers, which are compact coherent optical receivers conforming to the OIF implementation agreement.
- *4 MMI: An abbreviation of multi-mode interference. MMI also means a device that separates or synthesizes light through multi-mode interference.
- *5 DP-QPSK: An abbreviation of dual-polarization quadrature phase shift keying. A QPSK communication system simultaneously transmits 2 bits of information by using four phases.

Additional use of two orthogonally polarization (DP) enables this system to simultaneously transmit two QPSK signals, thereby transmitting a total of 4 bits of information.

*6 DP-16QAM: An abbreviation of dual-polarization 16 quadrature amplitude modulation. 16QAM is a communication system that simultaneously transmits 16 signals or 4 bits of information.
 Further use of two orthogonally polarization (DP)

enables this system to simultaneously transmit two 16QAM signals, thereby transmitting a total of 8 bits of information.

*7 MEMS: An abbreviation of micro electro-mechanical systems, referring to miniature devices that are made by integrating machine elements and electronic circuits on a single substrate using microfabrication technology.

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Contributors The lead author is indicated by an asterisk (*).

M. TAKECHI*

 Assistant General Manager, Transmission Devices Laboratory



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Y. TATEIWA • Sumitomo Electric Device Innovations, Inc.



M. KUROKAWA

Transmission Devices Laboratory



#### H. YAGI • Ph. D.

Assistant General Manager, Transmission Devices Laboratory



H. HARA

Group Manager, Transmission Devices Laboratory

